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123103

Teradyne

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PATENT TRADEMARK OFFICE

December 31, 2003

MS Patent Application
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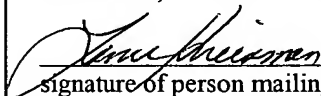
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Sir:

TRANSMITTAL FOR PATENT APPLICATION

Transmitted herewith for filing is a new patent application entitled:

“Silicon-On-Insulator Channel Architecture For ATE”

by inventor(s):

OSTERTAG, Edward A., , , ,

Enclosed are:

8 page(s) of specification
3 page(s) of claims
1 page(s) of abstract
5 sheet(s) of drawing(s)

FEE CALCULATION

Basic Filing Fee 10

	<u>Claims Filed</u>	<u>Excess Claims</u>	<u>Charge Per Claim</u>	<u>Totals</u>
Total:	10	0	18	0
Independent:	3	0	86	0
<i>Total Fee:</i>				\$ 770

Please charge the Total Fee to Teradyne, Inc.'s Deposit Account No. **20-0515**. The Commissioner is hereby authorized to charge fees under 37 CFR 1.16 and 1.17 which may be required, or credit any overpayment to Deposit Account No. 20-0515. One additional copy of this sheet is enclosed.

Please date stamp and return the enclosed self-addressed, stamped post card to evidence receipt of this application.

Respectfully Submitted,



Lance Kreisman
Attorney for Applicant
Reg. No. 39256

Atty. Docket No. : 1837-US
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